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These search terms have been highlighted: **otsuka simulator and simulation method**

invention: **Simulator and simulation method**

patentnr: EP1134638

pubdat: 2001-09-19

inventor: **OTSUKA** TORU (JP)

applicant: TOKYO SHIBAURA ELECTRIC CO (JP)

requested: [\_] EP1134638, A3

applnr: EP20010106046 20010312

prior: JP200000069232 20000313

ipclass: G05B19/418

ecclass:

equiv: [\_] US2001025235

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*Shioda, T.; Sekine, Y.; Otsuka, H.;*

Frequency Control Symposium and PDA Exhibition Jointly with the 17th Euro Frequency and Time Forum, 2003. Proceedings of the 2003 IEEE International May 2003

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*Shimizu, K.; Otsuka, K.;*

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*Koyama, T.; Zhu, Y.; Otsuka, T.; Takada, T.; Murooka, Y.;*

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**19 The evaluation of high-reliability adhesive film for T-BGA**
*Otsuka, M.; Yamagata, O.; Imoto, T.; Takano, T.; Takahashi, K.; Nakayoshi, I.*  
 Electronic Components and Technology Conference, 1998. 48th IEEE , 25-28 June 1998

1998  
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